

SERIAL NO.  
09/626,325

**APPLICANT**  
**John S. Yates Jr., et al.**

(Use several sheets if necessary)

**FILING DATE**  
**July 26, 2000**

GROUP ART UNIT  
2183

NOV 12 2004

## U.S. PATENT DOCUMENTS

EXAMINER Richard Ellis

**DATE CONSIDERED** 10/6/2005

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 114596-28-0053BS	SERIAL NO. 09/626,325
<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				APPLICANT John S. Yates Jr., et al.	
				FILING DATE July 26, 2000	GROUP ART UNIT 2183
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)</b>					
RLE			Brad Calder, Peter Feller, Alan Eustace, Value Profiling, Proceedings of 30th International Symposium on Microarchitecture (Micro-30), IEEE, pages 259-269 (December 1-3, 1997)		
RLE			Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264 (1999) <a href="http://www.alphalinux.org/archives/axp-list/June1999/0475.html">www.alphalinux.org/archives/axp-list/June1999/0475.html</a>		
RLE			Dean, ProfileMe: Hardware Support for Instruction-Level Profiling on Out-of-Order Processors," Proceedings of 30th Annual Intl. IEEE/ACM Symp. on Microarchitecture, pp. 292-302 (Dec. 1997)		
RLE			Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) <a href="http://www.digital.com/semiconductor/amt/fx32/fx-white.htm">http://www.digital.com/semiconductor/amt/fx32/fx-white.htm</a>		
RLE			Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)		
RLE			Kavi et al., A Performability Model for Soft Real-Time Systems, IEEE Proceedings of the 27th Annual Hawaii International Conference on System Sciences, pp. 571-579 (1994)		
RLE			Kim and Tyson: Analyzing the Working Set Characteristics of Branch Execution, Proceedings of the 31st Annual ACM/IEEE International Symposium on Microarchitecture, pp. 49-58 (Dec. 1998)		
RLE			Monica S. Lam, Robert P. Wilson, Limits of Control Flow on Parallelism, Proceedings of the 19th Annual International Symposium on Computer Architecture, p.46-57 (May 1992)		
RLE			Larus and Schnarr: EEL: Machine-Independent Executable Editing, EEL: Machine-independent executable editing. In Proceedings of the ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI), 1995		
RLE			M. Lipasti and J. Shen. Exceeding the Data-Flow Limit Via Value Prediction, 29th International Symposium on Microarchitecture, pages 226-237, IEEE (Dec. 1996)		
RLE			Veen: Dataflow Machine Architecture, ACM Computing Surveys vol. 18 no. 4 pp. 365-96 (December 1986)		
EXAMINER			DATE CONSIDERED		
Richard Ellis			10/6/2005		